WELLS ST. JOHN, P.S.

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#### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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- 3. PTO-1449

Dated: July 13 2004

Bv:

Jaime M. White

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#### **FEE DEFICIENCY**

Although it is believed that no fees are due, the Commissioner is hereby authorized to charge any fees under 37 C.F.R. 1.16 and 1.17 which may be required by this paper to Deposit Account No. 23-0925.

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Jennifer J. Taylor, P

R/eg. N/o. 48,71

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Application Serial No
Filling Date July 21, 2003
Inventor Luan C. Tran
Assignee Micron Technology, Inc.
Group Art Unit
priority Examiner Kennedy, Jennifer J.
Attorney's Docket No
Title: Methods of Forming Semiconductor Constructions

## SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References -See Attached Form PTO-1449

The attached form PTO-1449 is submitted in compliance with 37 CFR § 1.56. Copies of the cited art are included with the exception of U.S. patents and published U.S. applications (1276 OG 55). No admission is made regarding whether all the submitted references are prior art.

This Supplemental Information Disclosure Statement is being filed within three months of the filing date of the application or before the mailing date of a first Office Action, whichever occurs last. Therefore, no fee is believed to be However, in the event that a fee is required for filing this required. Supplemental Information Disclosure Statement, please charge the fee specified under 37 C.F.R. § 1.17(p) to Deposit Account No. 23-0925.

Respectfully submitted,

Dated: (

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